

# महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोस्वपुर

कक्षा : बी.एस-सी. भाग-तीन		पाठ्यक्रम योजना : सत्र-२०१८-१९		विषय : कम्प्यूटर साइंस	
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
16.07.18	1	Virendra Tiwari	I	Principle of comp. design	Basic components of computer system
17.07.18	2	Virendra Tiwari	I	Principle of Comp.design	Overview of Construction of ALU
18.07.18	1	Harvind Kr. Srivastav	III	Object Oriented System	Introduction to object oriented System
19.07.18	2	H.K.S	III	Object Oriented System	Overview of object oriented System
20.07.18	1	Virendra Tiwari	II	DBMS	Concept of Database and its evaluation
20.07.18	1	H.K.S	IV	Data Communication System	Transmission System
21.07.18	-		-	<b>CLASS TEACHING</b>	
21.07.18	2	H.K.S	IV	Data Communication System	Purpose, Components Source Transmitter
23.07.18	3	Virendra Tiwari	I	Principle Comp. Design	Overview of Operation of ALU
24.07.18	4	Virendra Tiwari	I	Register Transfer & Micro Operator	Register Transfer Language
25.07.18	3	H.K.S	III	OOS	Abstract data types
26.07.18	4	H.K.S	III	OOS	Inheritance
27.07.18	2	Virendra Tiwari	II	DBMS	Data Abstraction
27.07.18	3	H.K.S	IV	Data Communication System	Data Transmission: Frequency
28.07.18	-		-	<b>CLASS TEACHING</b>	
28.07.18	4	H.K.S	IV	Data Communication System	Spectrem & Band Width
30.07.18	5	Virendra Tiwari	I	Register Transfer & micro-Operater	Bus & Memory Transfer
31.07.18	6	Virendra Tiwari	I	Register Transfer & micro-operator	Logic micro operators
01.08.18	5	H.K.S	III	OOS	Polymorphism
02.08.18	6	H.K.S	III	OOS	Object Identity
03.08.18	3	Virendra Tiwari	II	DBMS	Data integration
03.08.18	5	H.K.S	IV	Data Communication System	Spectrem & Band Width
04.08.18	-		-	<b>CLASS TEACHING</b>	
04.08.18	6	H.K.S	IV	Data Communication System	Anolog Data Transmission
06.08.18	7	Virendra Tiwari	I	Register Transfer & micro-operator	Shift micro operators
07.08.18	8	Virendra Tiwari	I	Register Transfer	Bus Structure multiple Bus Hierarchies
08.08.18	7	H.K.S	III	OOS	Object modling concepts
09.08.18	8	H.K.S	III	OOS	Object oriented proگرامing language
10.08.18	4	Virendra Tiwari	II	DBMS	Data Models, Components of DBMS
10.08.18	7	H.K.S	IV	Data Communication System	Digital Data Tansmission

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11.08.18	-		-	<b>CLASS TEACHING</b>	
11.08.18	8	H.K.S	IV	Data Communication System	Data and Signals
12.08.18	9	Virendra Tiwari	I	Register Transfer & Micro-Operator	Eliment of bus designe
13.08.18	10	Virendra Tiwari	I	Register Transfer & Micro-Operator	PCI Bus Structure
16.08.18	9	H.K.S	III	OOS	Object oriented programing
17.08.18	10	H.K.S	III	OOS	Overview of C++ Programing
17.08.18	9	H.K.S	IV	Data & Signal	Anolog & Digital Signaling of anolog & digital data
18.08.18	5	Virendra Tiwari	II	DBMS	Three level of Architecture of DBMS
18.08.18	10	H.K.S	IV	Data & Signal	Modem
20.08.18	-		-	<b>CLASS TEACHING</b>	
21.08.18	11	Virendra Tiwari	I	Memory	Linear Structure Memory Organisation & Coders
23.08.18	12	Virendra Tiwari	I	Memory`	Dimension of Memory Access
24.08.18	11	H.K.S	III	C++ Programming	Support for data Obstraction
24.08.18	11	H.K.S	IV	Data & Signal	Modulation Techniques
25.08.18	12	H.K.S	III	OOS (C++)	Support for object oriented programing
25.08.18	12	H.K.S	IV	Data & Signal	CODEC, Digital Transmitter
27.08.18	6	Virendra Tiwari	II	DBMS	Data Models Implementation
28.08.18	-		-	<b>CLASS TEACHING/TEST</b>	
29.08.18	13	Virendra Tiwari	I	Memory	Connectivity memory chip to a computer trees
30.08.18	14	Virendra Tiwari	I	Memory	Magnetic Disk, Floppy Disk
01.09.18	13	H.K.S	III	C++ Programming Langauage	Declaration & Constatnts
01.09.18	13	H.K.S	IV	Data & signal	Transmission Impairments
04.09.18	14	H.K.S	III	C++ Programming Langauage	Declaration & Constatnts
05.09.18	7	Virendra Tiwari	II	DBMS	Relation Data
06.09.18	-			<b>CLASS TEACHING</b>	
07.09.18	15	Virendra Tiwari	I	Memory	Magnetic Tape
07.09.18	14	H.K.S	IV	Data & signal	Attemation & Attemation distrortion
08.09.18	16	Virendra Tiwari	I	I/O Device	Cassettes & Cartidge
08.09.18	15	H.K.S	IV	Data & signal	Delay Diotrortion, noise
10.09.18	15	H.K.S	III	C++ Programming Langauage	Function & File

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11.09.18	16	H.K.S	III	C++ Programming Language	Linkages
12.09.18	14	Virendra Tiwari	II	DBMS	Network Hierarchical
13.09.18	-			<b>CLASS TEACHING</b>	
14.09.18	16	Virendra Tiwari	I	I/O Device	Fundamentals of Keyboards, Printers
14.09.18	15	H.K.S	IV	Data & Signal	Introduction to network
15.09.18	15	Virendra Tiwari	I	I/O Device	CRT & LCD Displays Devices
15.09.18	16	Virendra Tiwari	IV	Data & Signal	TCP/IP Reference Method
17.09.18	8	H.K.S	III	Classes & Objects	Definition of class
18.09.18	17	H.K.S	III	Classes & Objects	Class Declaration
19.09.18	16	Virendra Tiwari	II	DBMS	Relation & Manipulation
20.09.18	-		-	<b>CLASS TEACHING/TEST</b>	
22.09.18	16	Virendra Tiwari	I	CPU	General Register & Organisation
22.09.18	19	H.K.S	IV	Transmission Media	Magnetic Media
24.09.18	18	Virendra Tiwari	I	CPU	Memory Stack
25.09.18	20	H.K.S	IV	Data & Signal	Class function definition member
26.09.18	19	H.K.S	IV	Data & Signal	Definition Outside the class, declaration, scope resolution operator
27.01.18	20	Virendra Tiwari	II	DBMS	Relational Algebra
28.09.18	-		-	<b>CLASS TEACHING</b>	
28.10.18	19	H.K.S	IV	Transmission Media	Twisted Pair cable
01.10.18	21	Virendra Tiwari	I	CPU	One Address & Two Address Instructions
03.10.18	22	Virendra Tiwari	I	CPU	Data Transfer
04.10.18	21	H.K.S	III	Class & Object	Private & Public member Functions
05.10.18	22	H.K.S	III	Class & Object	Nesting member function creating objects
05.10.18	20	H.K.S	IV	Transmission Media	Baseband & Broadband Coaxial Cables
06.10.18	11	Virendra Tiwari	II	DBMS	Relational Calculus SQL
06.10.18	21	H.K.S	IV	Transmission Media	Fiber Optics
09.10.18	-			<b>CLASS TEACHING</b>	
10.10.18	23	Virendra Tiwari	I	CPU	Arithmetic Logic & Shift
11.10.18	24	Virendra Tiwari	I	CPU	Software & Hardware Interrupt

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12.10.18	23	H.K.S	III	Calsses & Objects	Accesssing Member Functions
12.10.18	22	H.K.S	IV	Wireless Transmission	Radio Transmission
13.10.18	24	H.K.S	III	Classes & Objects	Accesssing class members
13.10.18	23	H.K.S	IV	Wireless Transmission	Radio Transmission
15.10.18	12	Virendra Tiwari	IV	DBMS	Funtional Dependencies Ist & 3rd
					Normal form
16.10.18	-			<b>CLASS TEACHING/TEST</b>	
17.10.18	25	Virendra Tiwari	I	CPU	Arithmetic & Instruction Pipr
22.10.18	26	Virendra Tiwari	I	Control Unit	Construction of instruction
24.10.18	25	H.K.S	III	Classes & Objects	Arrays of Objects
25.10.18	26	Virendra Tiwari	II	Classes & Objects	Objects as function agreement
26.10.18	13	Virendra Tiwari	II	Relation Database Design	BCNF
26.10.18	24	H.K.S	IV	Wireless Transmission	Microwave Transmission
27.11.18	-			<b>CLASS TEACHING</b>	
27.11.18	25	H.K.S	IV	Wireless Transmission	ISDN, ATM
29.10.18	27	Virendra Tiwari	I	Control Unit	Intruccion Cycle Organisation
31.10.18	28	Virendra Tiwari	I	Control Unit	Control Register
01.11.18	27	H.K.S	III	Operator Overloading	Opertaor Function, User Defined
					Type conversion
02.11.18	28	H.K.S	III	Operator Overloading	Literal , Large Object
02.11.18	26	H.K.S	IV	Wireless Transmission	Data Linklayer, Services, Framing
03.11.18	14	Virendra Tiwari	II	FoxPro	Introduction of FoxPro
03.11.18	27	H.K.S	IV	Wireless Transmission	Error control, error detecting & correcting
					codes
12.11.18				<b>CLASS TEACHING</b>	
13.11.18	29	Virendra Tiwari	I	Control unit	Execution cycle organisation of control
					registers
14.11.18	30	Virendra Tiwari	I	Control unit	Sequence of operation of control register
15.11.18	29	H.K.S	III	Operator Overloading	Assignment & Iinitialisation Subscriping
16.11.18	30	H.K.S	IV	Wireless Transmission	Data Links Protocols

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16.11.18	28	H.K.S	IV	Wireless Transmission	Data links protocols
17.11.18	15	Virendra Tiwari	II	FoxPro	Database Construction
17.11.18	29	H.K.S	IV	HDLC	Static & dynamic channel allocation in Lans
18.11.18	-			<b>CLASS TEACHING</b>	
20.11.18	31	Virenda Tiwari	I	Control unit	Controlling arithmetic operators
21.11.18	32	Virenda Tiwari	I	Control unit	Branch
22.11.18	31	H.K.S	III	Operator overloading	A String Class
23.11.18	32	H.K.S	III	Operator overloading	Inheritance through extending classes
23.11.18	30	H.K.S	IV	HDLC	Static & dynamic channel allocation in Lans
24.11.18	16	Virendra Tiwari	II	FoxPro	Searching
24.11.18	31	H.K.S	IV	Multiple Access Protocol	ALOHA, Congention Control algo
26.11.18	-			<b>CLASS TEACHING/TEST</b>	
27.11.18	33	Virendr Tiwari	I	Control Unit	Arithmetic operators
28.11.18	34	Virendr Tiwari	I	Control Unit	Skip or Jump Instruction
29.11.18	33	H.K.S	III	Inheritance through extending classes	Derived Class, defining drived classes
30.11.18	34	H.K.S	III	Inheritance through extending classes	Visibilty mode, Signal Inheritance
30.11.18	32	H.K.S	IV	Multiple access protocol	CSMA/CD, JEEE Standards 802.3
01.12.18	17	Virendra Tiwari	II	Foxpro	Sorting
01.12.18	33	H.K.S	IV	MAP	Ethernet,802.4, Token Bus
03.12.18	-			<b>CLASS TEACHING</b>	
06.12.18	35	Viendra Tiwari	I	Control Unit	Register Language
07.12.18	36	Viendra Tiwari	I	Control Unit	Micro-Programming
07.12.18	34	H.K.S	IV	MAP	Ethernet,802.4, Token ring, Bridges
08.12.18	35	H.K.S	III	Streaming& Templets & Design for Libral	C-I/O Formatting Files & Streams
08.12.18	35	H.K.S	IV	MAP	Routers, Gateways Routing algos
11.12.18	36	H.K.S	III	Steam Temples	C I/O Design of Liberties
12.12.18	18	Virendra Tiwari	II	DBMS	Indexing
13.12.18	-			<b>CLASS TEACHING</b>	
14.12.18	37	Virendra Tiwari	I	Control Unit	Micro-Programming
14.12.18	36	H.K.S	IV	MAP	Internet Working

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15.12.18	38	Virendra Tiwari	I	Control Unit	Micro-Programming
15.12.18	37	H.K.S	IV	MAP	TCP/IP Protocol
17.12.18	37	H.K.S	III	OOA & Design	Object Oriented Analysis System Design
18.12.18	38	H.K.S	III	OOA & Design	Object Design
19.12.18	19	Virendra Tiwari	II	Foxpro	Updation
20.12.18	-			<b>CLASS TEACHING</b>	
21.12.18	39	Virendra Tiwari	I	Control Unit	Program
21.12.18	38	H.K.S	IV	MAP	IP Addressing
22.12.18	40	Virendra Tiwari	I	Control Unit	Micro-Programming
22.12.18	39	H.K.S	IV	MAP	IP Addressing numericab
31.12.18	39	H.K.S	III	OOA & Design	Semantic
01.01.19	40	Virendra Tiwari	III	OOA & Design	Entity, Relationship
02.01.19	20	Virendra Tiwari	II	Foxpro	Data manage
03.01.19	-			<b>CLASS TEACHING</b>	
04.01.19	41	Virendra Tiwari	I	Control Unit	Shift Instruction
04.01.19	40	H.K.S	IV	MAP	Revision
05.01.19	42	Virendra Tiwari	I	Control Unit	Revision
07.01.19	41	H.K.S	III	OOA & Design	Modeling
08.01.19	42	H.K.S	III	OOA & Design	Booch's
09.01.19	21	H.K.S	III	O O A & Design	BOOCH's OOA, OOD approach
10.01.19	-			<b>CLASS TEACHING/TEST</b>	
11.01.19	43	Virendra Tiwari	I	Control Unit	Revision
11.01.19	41	H.K.S	IV	MAP	Problems
12.12.19	44	Virendra Tiwari	I	Principal of Computer Design	Revision
12.12.19	42	H.K.S	IV	MAP	Problems
17.12.19	43	H.K.S	III	OOA & Design	Semantic Rule
18.12.19	44	H.K.S	III	OOA & Design	Overview of Existing Methodlogy
18.12.17	22	Virendra Tiwari	II	FoxPro	Screen, Designing, Programming
19.01.18	-			<b>CLASS TEACHING</b>	
19.01.18	43	H.K.S	IV	MAP	Revision

