

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक		पाठ्यक्रम योजना : सत्र २०१७-२०१८			विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
15.07.2017	1	Virendra Tiwari	I	Introduction	Definition of Computer & about syllabus
17.07.2017	2	Virendra Tiwari	I	Introduction	Characteristic, evaluation
18.07.2017	1	Virendra Tiwari	II	Digital circuits	Logic gates (Introduction) & about syllabus
19.07.2017	2	Virendra Tiwari	II	Digital circuits	AND, OR Logic gates
20.07.2017	1	Sanjay Kumar Tiwari	III	Operating System	About syllabus
21.07.2017				Class Teaching	
22.07.2017	3	Virendra Tiwari	I	Introduction	Generation of computer
24.07.2017	4	Virendra Tiwari	I	Introduction	Types of computer
25.07.2017	3	Virendra Tiwari	II	Digital circuits	NAND. NOR Logic gate
26.07.2017	4	Virendra Tiwari	II	Digital circuits	Exclusive AND & XOR gates
27.07.2017	2	Sanjay Kumar Tiwari	III	Operating System overview	Computer System structure
29.07.2017				Class Teaching	
31.07.2017	5	Virendra Tiwari	I	Introduction	Major component of digital computer
01.08.2017	6	Virendra Tiwari	I	Introduction	Hardware
02.08.2017	5	Virendra Tiwari	II	Digital circuits	Universal Gates
03.08.2017	6	Virendra Tiwari	II	Digital circuits	Gates problems
04.08.2017	3	Sanjay Kumar Tiwari	III	Operating System overview	OS Structure
05.08.2017				Class Teaching	
08.08.2017	7	Virendra Tiwari	I	Introduction	Software, Firmware
09.08.2017	8	Virendra Tiwari	I	Introduction	Computer, Application
10.08.2017	7	Virendra Tiwari	II	Digital circuits	Introduction Boolean algebra
11.08.2017	8	Virendra Tiwari	II	Digital circuits	Example explanation of boolean algebra
12.08.2017	4	Sanjay Kumar Tiwari	III	Operating system overview	OS functioned, facilities
14.08.2017				Class Teaching	
16.08.2017	9	Virendra Tiwari	I	Data representation	Introduction
17.08.2017	10	Virendra Tiwari	I	Data representation	Decimal no. system, Binary no. system
18.08.2017	9	Virendra Tiwari	II	Digital circuits	Demorgham Law in Boolean algebra
19.08.2017	10	Virendra Tiwari	II	Digital circuits	Introduction k-map
21.08.2017	5	Sanjay Kumar Tiwari	III	Memory management	Introduction
22.08.2017				Class Teaching	

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23.08.2017	11	Virendra Tiwari	I	Data Representation	Decimal conversion to Binary conversion
24.08.2017	12	Virendra Tiwari	I	Data Representation	Octal number system
25.08.2017	11	Virendra Tiwari	II	Digital circuits	Two variable K-map
26.08.2017	12	Virendra Tiwari	II	Digital circuits	Example of two variable k.map
28.08.2017	6	Sanjay Kumar Tiwari	III	Memory management	Single Contiguous allocations
29.08.2017				Class Teaching	
31.08.2017	13	Virendra Tiwari	I	Data Representation	Hexadecimal no. system
01.09.2017	14	Virendra Tiwari	I	Data Representation	BCD number
02.09.2017	13	Virendra Tiwari	II	Digital circuits	Three variable k-map
05.09.2017	14	Virendra Tiwari	II	Digital circuits	Four variable k-map & don't care condition
07.09.2017	7	Sanjay Kumar Tiwari	III	Memory management	Partitioned allocation
07.09.2017				Monthly Evaluation	
08.09.2017	15	Virendra Tiwari	I	Data Representation	Binary arithmetic
09.09.2017	16	Virendra Tiwari	I	Data Representation	Complement, r & (r-1)'s complements
11.09.2017	15	Virendra Tiwari	II	Combinational circuits	Introduction
12.09.2017	16	Virendra Tiwari	II	Combinational circuits	Half adder
13.09.2017	8	Sanjay Kumar Tiwari	III	Memory management	Relocation partitioned allocation
14.09.2017				Class Teaching	
15.09.2017	17	Virendra Tiwari	I	Data Representation	Fixed point & floating point representation
16.09.2017	18	Virendra Tiwari	I	Introduction to programming	Concept of algorithm
18.09.2017	17	Virendra Tiwari	II	Combinational circuits	Full adder
19.09.2017	18	Virendra Tiwari	II	Combinational circuits	Introduction of flip-flops
21.09.2017	9	Sanjay Kumar Tiwari	III	Memory management	Introduction paging
22.09.2017				Class Teaching	
23.09.2017	19	Virendra Tiwari	I	Introduction to programming	Flowchart, dataflow diagram
25.09.2017	20	Virendra Tiwari	I	Programming in C	An overview of C
26.09.2017	19	Virendra Tiwari	II	Combinational circuit	RS-Flip-Flop
27.09.2017	20	Virendra Tiwari	II	Combinational circuit	D-Flip-Flop
03.10.2017	10	Sanjay Kumar Tiwari	III	Memory management	Example or problems on paging
04.10.2017				Class Teaching	

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06.10.2017	21	Virendra Tiwari	I	Programming in C	Structure of C program
07.10.2017	22	Virendra Tiwari	I	Programming in C	Storage class specifier
09.10.2017	21	Virendra Tiwari	II	Combinational circuit	J.K. flip-flop
10.10.2017	22	Virendra Tiwari	II	Combinational circuit	T- flip-flop
11.10.2017	11	Sanjay Kumar Tiwari	III	Memory management	Segmentation, demand page allocation
12.10.2017				Class Teaching	
13.10.2017	23	Virendra Tiwari	I	Programming in C	Data types, variable declaration
14.10.2017	24	Virendra Tiwari	I	Programming in C	Operator & Expressions
16.10.2017	23	Virendra Tiwari	II	Combinational circuit	Edge-triggered flip-flop
23.10.2017	24	Virendra Tiwari	II	Combinational circuit	Encoders
24.10.2017	12	Sanjay Kumar Tiwari	III	Processor management	State models, case study of DOS
25.10.2017				Monthly Evaluation	
26.10.2017	25	Virendra Tiwari	I	Control Statement	True & false in C
27.10.2017	26	Virendra Tiwari	I	Control Statement	Conditional statement, if
28.10.2017	25	Virendra Tiwari	II	Combinational circuits	Decoders
30.10.2017	26	Virendra Tiwari	II	Combinational circuits	Multiplexers
31.10.2017	13	Sanjay Kumar Tiwari	III	Processor management	Job schedulers and its type, Windows
					Operating System
01.11.2017				Class Teaching	
02.11.2017	27	Virendra Tiwari	I	Control statements	For statement, while statement
03.11.2017	28	Virendra Tiwari	I	Control statements	do-while & break
06.11.2017	27	Virendra Tiwari	II	Combinational circuits	Introduction Register
07.11.2017	28	Virendra Tiwari	II	Combinational circuits	Shift Register
08.11.2017	14	Sanjay Kumar Tiwari	III	Processor Management	Process Scheduling
10.11.2017				Class Teaching	
11.11.2017	29	Virendra Tiwari	I	Control Statement	Continue exit () & goto
13.11.2017	30	Virendra Tiwari	I	Basic I/O	Formatted & Unformatted I/O
14.11.2017	29	Virendra Tiwari	II	Combinational circuits	Buffer register counters
15.11.2017	30	Virendra Tiwari	II	Combinational circuits	Ripple counters
16.11.2017	15	Sanjay Kumar Tiwari	III	Processor management	Job and process synchronisation

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17.11.2017				Class Teaching	
18.11.2017	31	Virendra Tiwari	I	Basic I/O	Concept of function
20.11.2017	32	Virendra Tiwari	I	Basic I/O	Examples of different types of functions
21.11.2017	31	Virendra Tiwari	II	Basic computer organisation	Instruction code
22.11.2017	32	Virendra Tiwari	II	Basic computer organisation	Direct & indirect address
23.11.2017	16	Sanjay Kumar Tiwari	III	Processor Management	Race conditions, Scheduling algorithm & its related problems
24.11.2017				Monthly Evaluation	
25.11.2017	33	Virendra Tiwari	I	Basic I/O	Recursion
27.11.2017	34	Virendra Tiwari	I	Basic I/O	Parameter passing technique
28.11.2017	33	Virendra Tiwari	II	Basic computer organisation	Timing & control signals
29.11.2017	34	Virendra Tiwari	II	Basic computer organisation	Instruction Cycle
30.11.2017	17	Sanjay Kumar Tiwari	III	Processor management	Introduction dead lock, Dead lock detection Recovery & Prevention
01.12.2017				Class Teaching	
02.12.2017	35	Virendra Tiwari	I	Array	Introduction
03.12.2017	36	Virendra Tiwari	I	Array	One dimensional
06.12.2017	35	Virendra Tiwari	II	Basic computer organisation	Memory reference instruction
07.12.2017	36	Virendra Tiwari	II	Basic computer organisation	I/O Instruction
08.12.2017	18	Sanjay Kumar Tiwari	III	Device Management	Introduction & its different technique
09.12.2017				Class Teaching	
11.12.2017	37	Virendra Tiwari	I	Array	Two dimensional, multi dimensional array
12.12.2017	38	Virendra Tiwari	I	Pointers	Concept of pointers, Pointer operators, Pointer expressions, pointer & array
13.12.2017	37	Virendra Tiwari	II	Memory	Memory hierachy, RAM, SRAM, DRAM
14.12.2017	38	Virendra Tiwari	II	Memory	Memory write a ability & storage performance
15.12.2017	19	Sanjay Kumar Tiwari	III	Device management	I/O Device storage device, logical & physical, file system
16.12.2017				Class Teaching	
18.12.2017	39	Virendra Tiwari	I	Pointer	Strings & string library function

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19.12.2017	40	Virendra Tiwari	I	Structure of unions	Concepts, Structure & function array
					structure, structure pointer, Nested structure
20.12.2017	39	Virendra Tiwari	II	Memory	Memory units
21.12.2017	40	Virendra Tiwari	II	Memory	Masked programmed ROM
					Cache memory, virtual memory concept
22.12.2017	20	Sanjay Kumar Tiwari	III	Information management	File system
23.12.2017				Monthly Evaluation	
26.12.2017	41	Virendra Tiwari	I	File management	Definitions, opening & closing of a file
27.12.2017	41	Virendra Tiwari	II	Memory	Flash Memory
28.12.2017	42	Virendra Tiwari	II	Memory	Memory Type - ROM
29.12.2017	21	Sanjay Kumar Tiwari	III	Information Management	Case Study of DOS
30.12.2017	22	Sanjay Kumar Tiwari	III	Information Management	Case Study of Windows
01.01.2018				Class Teaching	
02.01.2018	42	Virendra Tiwari	I	File Management	Opening of a File
03.01.2018	42	Virendra Tiwari	II	Memory	EPROM
04.01.2018	43	Virendra Tiwari	II	Memory	EPROM
05.01.2018	23	Sanjay Kumar Tiwari	III	File System	Logical File System
06.01.2018	44	Virendra Tiwari	II	Transister	Transister as a Switch
08.01.2018				Class Teaching	
09.01.2018	24	Sanjay Kumar Tiwari	III	File System	Physical File System
10.01.2018	45	Virendra Tiwari	II	Transister	Logical Circuit
11.01.2018	46	Virendra Tiwari	II	Transister	Active & Passive Logic Circuit
12.01.2018	47	Virendra Tiwari	II	Transister	Diode Logical, RTL, CMOS